UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. : 7,334,110 B1 Page 1 of 2

APPLICATION NO.: 10/643586

DATED: February 19, 2008

INVENTOR(S): Faanes et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On page 2, under "Other Publications", in column 2, line 30, delete "Architecutre" and insert -- Architecture --, therefor.

On page 2, under "Other Publications", in column 2, line 39, delete "Interconnection" and insert -- Interconnection --, therefor.

On page 2, under "Other Publications", in column 2, line 43, delete "Airthmetic" and insert -- Arithmetic --, therefor.

On page 2, under "Other Publications", in column 2, line 58, delete "Stepsinto" and insert -- Steps into --, therefor.

On page 4, under "Other Publications", in column 1, line 6, delete "acton" and insert -- action --, therefor.

In column 5, line 40, delete "ecache" and insert -- Ecache --, therefor.

In column 6, line 40, delete "modem" and insert -- modern --, therefor.

In column 11, line 30, delete "be" and insert -- been --, therefor.

In column 15, line 23, delete "(VBFlage)" and insert -- (VBFlag) --, therefor.

In column 16, line 48, after "instruction" insert -- . --.

In column 18, line 41, delete "element" and insert -- elements --, therefor.

In column 19, line 23, after "that" delete "is" and insert -- are --, therefor.

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It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 22, line 62, in Claim 12, after "dispatches" delete "a".

Signed and Sealed this

First Day of July, 2008

JON W. DUDAS Director of the United States Patent and Trademark Office